Customer No.: 31561
Application No.: 10/630,983
Docket No.: 15650-US-PA

AMENDMENTS

To the Claims:

The listing of claims will replace all prior versions of claims in the application:

1-6. (cancelled)

7. (previously presented) A noise filter for an integrated circuit comprising:

a transition circuit having an input and an output, said input of said transition circuit being coupled with an input pad of said integrated circuit, said output of said transition circuit being coupled with an input buffer;

a first capacitor being inserted between said output of said transition circuit and a first voltage source; and

a second capacitor being inserted between said output of said transition circuit and a second voltage source;

said transition circuit comprising with a Pch MOS transistor and a Nch MOS transistor;

the source of said Pch MOS transistor and the Nch MOS transistor being coupled with the input of said transition circuit;

the drain of said Pch MOS transistor and the Nch MOS transistor being coupled with the output of said transition circuit;

the gate of said Pch MOS transistor being coupled with a third voltage source;

the gate of said Nch MOS transistor being coupled with a fourth voltage source.

8. (original) A noise filter according to Claim 7, wherein said integrated circuit is a LSI.

9. (original) A noise filter according to Claim 7, wherein said integrated circuit is a VLSI.

10. (original) A noise filter according to Claim 7, wherein said transition circuit includes two transfer gates, the two transfer gates are a NMOS transistor and a PMOS transistor.

11. (original) A noise filter according to Claim 7, wherein said input buffer is a schmitt trigger.

12. (original) A noise filter according to Claim 7, wherein said first voltage source is VDD.

13. (previously presented) A noise filter according to Claim 12, wherein the third and fourth voltage sources of said transition circuit are VDD/2.

14. (original) A noise filter according to Claim 12, wherein said second voltage source is VSS.